

<b>Notice of References Cited</b>	Application/Control No. 09/530,549		Applicant(s)/Patent Under Reexamination MAYER, ALBRECHT	
	Examiner Dwin M Craig		Art Unit 2123	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,960,188 A	09-1999	Linke et al.	703/14
*	B	US-6,223,144 B1	04-2001	Barnett et al.	703/22
*	C	US-5,870,588 A	02-1999	Rompaey et al.	703/13
*	D	US-6,014,512 A	01-2000	Mohamed et al.	703/27
*	E	US-5,680,584	10-1997	Herdeg et al.	703/22
*	F	US-5,680,542	10-1997	Mulchandani et al.	714/28
*	G	US-5,689,684	11-1997	Mulchandani et al.	703/23
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	H. Hsieh, L. Lavagno, C. Passerone, "Modeling micro-controller peripherals for high-level co-simulation and synthesis", IEEE 1997, pages 127-130.			
	V	Carl E. Wick, "Teaching Embedded Computer Systems with a Windows-Based Simulator", IEEE 1996, pages 242-245.			
	W	J-P Soininen, T. Huttunen, K. Tiensyrja, H. Heusala, "Cosimulation of Real-Time Control Systems" IEEE 1995, pages 170-175.			
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.